

# PATENT ABSTRACTS OF JAPAN

(11)Publication number : 2003-163623

(43)Date of publication of application : 06.06.2003

(51)Int.Cl. H04B 7/15  
H04L 1/00

(21)Application number : 2001-359757

(71)Applicant : HITACHI KOKUSAI ELECTRIC INC

(22)Date of filing : 26.11.2001

(72)Inventor : MIZUNO ATSUSHI

## (54) DIGITAL MODULATION RADIO REPEATER

### (57)Abstract:

**PROBLEM TO BE SOLVED:** To provide a digital modulation radio repeater capable of measuring accurate BER at all times even during a relay operation.

**SOLUTION:** In a digital modulator 1, by a multiplex circuit 4, PRBS signals supplied from a PRBS signal generator are time-division-multiplexed to digital input signals DI. In a digital demodulator 2, the PRBS signals are demultiplexed and taken out from digital signals transmitted through a radio channel 9 by a demultiplex circuit 7 and a bit error detector 8 measures a bit error rate BER generated in the radio channel 9 by the PRBS signals. Thus, while relaying the digital signals, the bit error rate BER is measured in real time.

